

CONTACT PROBE STORAGE FET SENSOR

BACKGROUND OF THE INVENTION

[0001] The present invention relates generally to a sensor arrangement which is based on the use of a FET (Field Effect Transistor) which responds to changes in distance from a substrate that emits an electric field and which, due to its rapid response characteristics, finds application in at least Contact Probe Storage (CPS) arrangements.

[0002] It has been hitherto been proposed to sense data which is written onto a movable medium using a probe that is supported on a cantilever and used to contact the medium. By using heat transfer characteristics between the movable medium and the probe (or a portion of the cantilever), it is possible to determine minute changes in distance between the movable medium and the cantilever on which the probe is carried, and use this as a means for reading out the data stored on the movable medium.

[0003] In the above type of arrangement, both imaging and reading are carried out using a thermomechanical sensing concept. A heater in the cantilever that is used for writing is also used as a thermal readback sensor by exploiting a temperature-dependent resistance function. That is to say, in this type of arrangement, the resistance (R) increases nonlinearly with heating power/temperature from room temperature to a peak value of 500 – 700°C. The peak temperature is determined by the doping concentration in the heater platform, which ranges from 1×10^{17} to 2×10^{18} . Above the peak temperature, the resistance drops as the number of intrinsic carriers increases because of thermal excitation.

[0004] For sensing, the resistor is operated at about 200°C. This temperature is not high enough to soften the polymer medium, as is necessary for writing, but allows the molecular energy transfer between the cantilever on which the probe is carried, and the moving medium, to remove heat and thus provide a parameter which allows the distance between the cantilever on which the probe is carried and the medium on which the probe is running to be measured.

[0005] That is to say, this thermal sensing is based on the fact that the thermal conductance between the heater platform and the storage substrate changes according to the distance between them. The medium between a cantilever and the storage substrate, in this case air, transports heat from the heater/cantilever to the storage media/substrate. When the distance between heater and sample is reduced as the probe moves into a bit indentation, heat is more efficiently transported through the air and the heater's temperature and hence its resistance decreases. Thus, changes in temperature of the continuously heated resistor are monitored while the cantilever is scanned over data bits, providing a means of detecting the bits.

[0006] Under typical operating conditions, the sensitivity of the thermomechanical sensing is even better than that of piezoresistive-strain sensing inasmuch as thermal effects in semiconductors are stronger than strain effects. A $\Delta R/R$ sensitivity of about 10^{-4} /nm is demonstrated by the images of the 40-nm-size bit indentations. This is better than the results are obtained using the piezoresistive-strain technique.

[0007] Nevertheless, the thermal response has been found to be slower than desired and is significantly slower than the cantilever's ability to mechanically follow the data pattern written in the medium. This leads to the system's read performance being slower than it would be if it were not limited to the thermal response of the sensing system.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] Fig. 1 is a schematic perspective view of an embodiment of a FET sensor arrangement according to the present invention;

[0009] Fig. 2 is a schematic sectional view taken along section line II – II of Fig. 3 showing the arrangement according to the first embodiment of the invention.

[0010] Fig. 3 is a probe side plan view of the arrangement depicted in Fig. 1.

[0011] Fig. 4 is a schematic sectional view taken along section line IV – IV of Fig. 5 showing the arrangement according to a second embodiment of the invention.

[0012] Fig. 5 is a underside plan view of the second embodiment of the invention.

DETAILED DESCRIPTION OF THE EXEMPLARY EMBODIMENTS

[0013] Figs. 1-3 show a first embodiment of the invention. Fig. 2 shows a suitable medium 101 such as a layer of polycarbonate or polymethylmethacrylate (PMMA) for example, which is formed over the surface of a suitable support substrate 102. The media 101, which in this case is non-conductive, has been heated (for example) locally to write data by forming a change in medium topography which can be detected by lowering a probe 104, which is formed at the end of the cantilever 106 in the manner depicted in Fig. 1, onto the medium.

[0014] Although the topography is shown as comprising a series of data recesses or pits 101A, these can be replaced with humps (not shown) or a combination of pits and humps. However, it should be noted that these humps (if used) should be isolated from the pits so as not to be confused with the raised ring that tends to form around the mouth of the pits like a lunar impact crater.

[0015] Thus, when the medium or the cantilever has been moved relative to the other to assume an exact coordinate relationship, the cantilever can be moved

toward the medium. In the event that a pit 101A (or isolated hump) is located under the probe 104, the entry of the probe into the pit (or engagement with the top of the hump) indicates the presence of a data bit. Should the probe 104 not find a pit or a hump and seat on the flat surface of the medium then an absence of a data bit is indicated.

[0016] Additionally, the bits may be coded such that a particular change from one state (pit, hump or flat surface) to another state would indicate a bit and other states or changes would indicate the lack of a bit. The invention can use other coding techniques employed in contact storage device detection patterns or other responses that are predominant in the response of the sensor-media systems.

[0017] The topography of the medium 101 is thus such that the distance or air gap between the medium 101 and a cantilever 106 on which the probe 104 is formed, varies. This distance variation allows a FET (field effect transistor) 108, which is formed in the end of the cantilever 106 proximate the probe 104, to respond to changes in an electric field which is generated between the substrate 102 and the cantilever 106 and thus modulate a signal in the form of a current which passes through the FET 108 in accordance with the amount of clearance between the medium 101 and the cantilever 106.

[0018] A sensor circuit 110 is arranged to be responsive to the change in current passing through the FET 108 and thus detect the change in distance between the cantilever 106 and the medium 101.

[0019] More specifically, in this first embodiment, the source, drain and substrate 102 are circuited as schematically illustrated in Fig. 2, so as to develop a bias voltage between the source and drain of the FET 108 and the medium 101. This induces the situation where the proximity of the substrate 102 effectively gates the FET 108 and modulates the amount of current which is permitted to flow from the source to the drain through the channel which interconnects the two.

[0020] Further, since the probe 104 moves over/contacts an electrically non-conductive medium 101, the modulation of the current passing through the FET

108 is due solely to the changes in the electrical field which are produced between the cantilever 106 and the substrate 102 which of course must be sufficiently conductive to allow for the required electrical field to be established. The cantilever 106, in this embodiment, is formed of silicon which has been doped in a known manner to produce the electrically conductive lines or traces 118A and 118B the source, drain and the channel 120.

[0021] The FET 108 in the illustrated embodiment is a depletion mode N-channel type FET. However, the embodiment is not limited to this type of FET and may be replaced with a P-channel type if desired. In depletion mode FETs, the channel is formed by doping in the channel region. The FET will conduct in varying amounts as modulated by the gate voltage. If the gate voltage is made negative enough, the carriers will be driven from the channel causing the FET to cease to conduct.

[0022] The probe 104 is formed of silicon or other suitable material and is formed using a suitable masking and etching technique.

[0023] In the embodiment illustrated in Figs. 1 – 3, the medium 101 and the cantilever 106 are operatively (mechanically) connected so that medium 101 is selectively movable with respect to the cantilever 106 by way of a drive mechanism denoted by element 119 (schematically depicted in Fig. 2). This mechanism is arranged to move the two elements (viz., the cantilever 106 and the medium 101) with respect to one another to as to assume a selected coordinate relationship and position the probe 104 so that it can detect if a data indicative change in topography (e.g. a pit 101A) is present or absent at that set of coordinates.

[0024] The cantilever 106 is etched out of silicon and, as shown in Fig. 1, extends from a base block 106A and is provided with layers of cantilever activation material 106B which are formed of intrinsically stressed material and which are used to induce flexure in the cantilever 106 and move the probe 104 with respect to the medium 101. The terminal ends of the doped traces 118A and 118B are located on the base block 106A and a provided with electrical contacts

118E. The activation material 106B, however, is not limited to the use of intrinsically stressed material and can be alternatively formed of a piezoelectric material if so desired.

[0025] Inasmuch as the above mentioned doping and etching techniques are well within the purview of the person skilled in the art of semiconductor fabrication, no further description will be given for brevity.

[0026] With this embodiment, a good sensor bandwidth can be expected inasmuch as the electric field responds as fast as the cantilever can move. The FET is thus able to respond quickly to the variations in field strength and has the potential to make the mechanics of the cantilever the limiting factor in the bandwidth. The signal to noise (SNR) for this arrangement can be expected to be improved as compared to the above-mentioned thermal type sensor in that, with the latter, much of the useful signal is filtered out by the thermal lowpass function.

[0027] Since the FET is capable of producing gain, the sensor can be expected to produce a relatively large output signal with respect to the various noise sources and thus reduce signal degradation due to these noises. Further, since the sensor does not require heat to work, the power consumption can be expected to be significantly lower.

[0028] Figs. 4 and 5 show a second embodiment of the invention which utilizes another type of type of FET 208. This is an induced-channel type FET. Unlike the FET 108 of the first embodiment, this induced-channel or enhancement mode FET is such that there is no intrinsic channel and the drain to source conductance is very low until the gate voltage is applied. When the gate voltage exceeds a given threshold, enough carriers are pulled into the channel region that the device starts to conduct. In an N-channel enhancement type FET, the channel is p-type material that forms a conduction band when sufficiently positive gate voltage is applied. When conducting, the channel behaves like n-type material.

[0029] Thus, the induced-channel FET 208 used in the second embodiment conducts current from source to drain in response to the electric field that is

created when a voltage is applied to the gate (viz., the substrate 102) and a channel being induced by the migration of free electrons. As with the FET of the first embodiment, the gate signal determines the amount of current flow through the channel as long as the source and drain voltages remain constant. When the gate voltage is at zero, essentially no current flows since a gate voltage is required to form a channel.

[0030] Although the invention has been disclosed with reference to a limited number of embodiments, the various modifications and variations which can be made without departing from the scope of the invention, which is limited only by the appended claims, will be self-evident to those skilled in the art of Atomic Resolution Storage (ARS) and Contact Probe Storage (CPS) technology. While the embodiments of the invention will find immediate application in mass storage devices where a large number of cantilevers are fabricated into a single reading unit for simultaneously reading the presence/absence of data indicative changed in medium topography, it should be appreciated that the use of the above disclosed FET arrangement can be used in connection with as sensors such as those used in pressure sensors, acceleration (G) sensors, microphones and position control systems.